End of Result Set

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L2: Entry 4 of 4   File: USPT   Jan 3, 1995

DOCUMENT-IDENTIFIER: US 5379431 A
TITLE: Boot framework architecture for dynamic staged initial program load

Detailed Description Text (135):
A set of DMA related services are available to device interface ensembles. These services include: (a) Dynamic assignment of DMA channels to clients; (b) Support for scattered-read, gathered-write (also known as scatter-gather); (c) Encapsulation of hardware limitation (such as the 24 bit addressing limit of ISA bus hardware). The DMA services allow DMA devices on the systems by providing an intermediate buffer in the preferably lower 16 Mb of physical memory. Since this capability slows down the data transfer, it is optional; and (d) DMA services classes may be subclassed to support third-party bus masters.

Detailed Description Text (270):
The BootConductor program will consist of an ordered sequence of operations, each encapsulated in a routine of the program. This section lists the sequence of operations.